Q.P. Code: 16EC417

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SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS)

B. Tech III Year I Semester Supplementary Examinations August-2022 LINEAR IC APPLICATIONS

(Common to EEE & ECE)

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Ti	me	e: 3 hours	ax. Mar	ks: 60					
		(Answer all Five Units $5 \times 12 = 60$ Marks)							
		UNIT-I							
1	a	a Compare different configurations of differential amplifier.							
	b	Explain the dual input balanced output differential amplifier with a neat circuit	L2	6M					
		diagram.							
		OR							
2		Draw and explain the various functional blocks of an operational amplifier IC.	L2 L2	6M					
	b Discuss the DC characteristics of an OP-AMP in detail?								
		UNIT-II							
3		Define input bais current, input offset current and input offset voltage.	L2	6M					
	b	Explain about the pole zero frequency compensation network.	L2	6M					
		OR							
4		Draw and Explain the high frequency equivalent model of the op-amp.	L2	6M					
	b	Explain about the unity gain bandwidth product that how influences the	L2	6M					
		frequency response.							
_		UNIT-III	T 4	0.5					
5		What is voltage follower? What are its features and applications?	L1	6M					
	D	Explain & Derive the expression for 2 input subtractor amplifier with circuit	L2	6M					
diagram. OR									
6	ล	Write short notes on V-I and I-V converters using op-amps.	L1	6M					
Ū		Derive the expression of Current to Voltage Converter.	L1	6M					
	~	UNIT-IV		01.1					
7	ล	Draw and explain the operation of Wein bridge oscillator and derive its	L2	6M					
,	u	frequency expression.	112	OIVI					
	b	Generate a triangular wave from the square wave with a neat expressions.	L1	6M					
		OR							
8	a	Discuss the applications of Astable multivibrator.	L1	6M					
	b	Draw the block diagram of PLL and explain its operation.	L1	6M					
		UNIT-V							
9	a	Draw and explain the weighted resistor DAC.	L1	6M					
	b	Explain about ladder type DAC.	L1	6M					
		OR							
10	E	xplain about counter type ADC with neat block diagram.	L2	12M					